

Industry Sponsored

M.Tech. Collaborative Program in Signal Processing and Communication Engineering



INFORMATION BOOKLET- JNTUK

**Advance your career with Industry Sponsored & Job guaranteed
M. Tech. in Signal Processing and Communications.**

Jawaharlal Nehru Technological University Kakinada Campus is first established on 16th July 1946 as College of Engineering, Vizagapatam at Kakinada by the Government of Madras State. Later in the Year 1972, it has become an important constituent unit of Jawaharlal Nehru Technological University, Hyderabad which is established as the first Technology University in the Country after the name of first Prime Minister of India, Pundit Jawaharlal Nehru. It has become a separate University in August 2008 by Government of Andhra Pradesh. Over the years JNTUK Kakinada Campus has produced highly professional and competitive engineers by infusing greater quality and content into the curriculum and educating the students with appropriate skills suitable for the rapidly developing Industry in India in the fields of Engineering, Technology, Science, Pharmacy and Management. JNTU Kakinada campus has crossed several mile stones in pursuits of academic excellence and has emerged as a trendsetter in the field of technical education. Its reputation for academic excellence attracts students across the Globe.

Avantel is a leading technology-driven company specializing in the design, development, and manufacturing of Electronics, Communication and Satellite Communication systems. Avantel is a well performing Limited Company listed in stock market. Avantel serves critical sectors such as Defence, Aerospace, and Communication, offering innovative and customized solutions. The company is recognized for its in-house R&D and strategic alliances with national institutions like ISRO and DRDO.

JNTUK, Kakinada has signed MOA with M/s Avantel, for Two years Collaborating Sponsored Program M. Tech in Signal Processing and Communications. A Centre of Excellence (CoE) with necessary infrastructure for this course will be established at JNTUK by Avantel. Avantel will depute two Laboratory Assistants and one co-ordinating Avantel Chair Professor sponsored by Avantel.

Eligibility and Process of Selection:

- ❖ B. Tech in Electronics and Communication Engineering (or) equivalent with a valid GATE/PGCET score.
- ❖ Selection of candidates will be done by giving weightage of 60% to GATE/PGECET and 40% weightage to interview performance.
- ❖ The interview will be conducted jointly with experts from JNTUK and Avantel.
- ❖ Admitted students should accept the terms and conditions of both Avantel and JNTUK for internship and thereafter employment offer as per HR policy of Avantel.

Program Highlights

Cutting – Edge Curriculum

Development in Collaboration with Industry leaders, bridging the gap between theory and application.

Expert Mentorship

Learn from JNTUK faculty and seasoned professionals at M/s Avantel.

State-of-the-Art Resources

A Centre of Excellence (CoE) with necessary infrastructure for this course at JNTUK Sponsored by M/s Avantel.

Career Assurance

Full time employment to the eligible, Successful and Competent students post successful Completion of Course.

What Make this Program Unique

First Year: Candidates will be trained in Signal Processing and Communications by senior professors in a Centre of Excellence established by Avantel.

Second-Year Industry Internship (with Stipend of Rs.25, 000 per month) with hands-on experience through a Year-long Internship at Avantel, working on cutting – edge projects that prepare you for the tech industry's challenges.

Guaranteed Pre- Placement offer

Full time employment to the eligible, Successful and Competent students post successful Completion of Course in the M/s Avantel.

Who Can Apply

- ❖ **B. Tech ECE or Equivalent Graduates with a strong passion for Signal Processing.**
- ❖ **Aspiring technologists who want to merge academic excellence with industry exposure**
- ❖ **Selection Pattern:**
Weightage
GATE/PGCET Score-60% and
Interview – 40%

Apply on or before: 01-09-2025, 5.00 PM.