

**A Five-Day Online
Faculty Development Program
On
Recent Industry Trends in
VLSI & Embedded Systems
(25th to 29th APRIL 2026)**



**Organized by
Department of
Electronics and Communication Engineering
UNIVERSITY COLLEGE OF ENGINEERING
JNTUK – NARASARAOPET
Approved by AICTE, New Delhi & Affiliated to JNTU Kakinada
Kakani (V), Narasaraopet– 522603, Palnadu (Dt.),
Andhra Pradesh.**

About JNTUK

Jawaharlal Nehru Technological University Kakinada (JNTUK) was established in 2008 under Act No. 30 of 2008 by the Government of Andhra Pradesh. It evolved from the College of Engineering, Visakhapatnam, founded in 1946 and later became a constituent college of JNTU Hyderabad in 1972. After the trifurcation of JNTU Hyderabad, it was reorganized as JNTUK in 2008. The University is located on a 100-acre campus in Kakinada and oversees engineering and technical education across 11 districts, including East Godavari, Krishna, Guntur, and Prakasam. JNTUK has about 159 affiliated colleges and serves nearly 2.3 lakh students through more than 50 undergraduate programs, over 120 postgraduate programs, and various doctoral research programs.

About the Institute

The JNTUK University College of Engineering Narasaraopet (UCEN) was established in June 2016 as a constituent college of Jawaharlal Nehru Technological University Kakinada. The institution was founded with the objective of providing quality technical education to meet the growing demands of industry and society. The college initially commenced with five undergraduate programs: Civil Engineering, Electrical and Electronics Engineering, Mechanical Engineering, Electronics and Communication Engineering, and Computer Science and Engineering. The Government of Andhra Pradesh allocated 85.94 acres of land at Kakani village, Narasaraopet Mandal, under G.O.Ms. No. 72, for the establishment of the permanent campus. The campus is situated in a serene environment near the foothills of Kotappakonda, providing an ideal atmosphere for academic and research activities. Prior to shifting to its permanent campus, the college functioned temporarily from the NBT Degree College campus on Palnadu Road, Narasaraopet. Over the years, the institution has developed into a center for academic excellence, equipped with modern infrastructure, laboratories, and qualified faculty, fostering innovation and research among students.

About the department

The Department of Electronics and Communication Engineering (ECE) at JNTUK University College of Engineering Narasaraopet has been functioning since the inception of the institution. The department has been consistently contributing to academic excellence through a strong teaching-learning process and dedicated faculty. The department is equipped with well-qualified and experienced faculty members, supported by adequate infrastructure and laboratory facilities. It offers an undergraduate program in Electronics and Communication Engineering with an annual intake of 60 students. The laboratories are well established with the necessary equipment to support practical learning in core areas enabling students to gain hands-on experience aligned with the academic curriculum. The department is actively involved in academic and research activities in emerging domains including VLSI Design, Communication Systems, Signal Processing, Image Processing, Embedded Systems, and Antennas & Wave Propagation. The department is supported by qualified technical staff who assist in the effective conduct of laboratory sessions and academic activities. The department regularly organizes Faculty Development Programs, workshops, and seminars to bridge the gap between academia and industry.

About the FDP

This FDP offers an overview of recent trends in VLSI & Embedded Systems, providing exposure to industry-oriented concepts through expert sessions from academia and industry. It focuses on emerging technologies, current practices, and application perspectives while bridging the gap between academic learning and industry requirements.

Objectives of the FDP

1. To provide participants with an understanding of recent trends, technologies, and practices in the broad domains of VLSI & Embedded Systems.
2. To enhance the pedagogical and technical capabilities of participants for effective teaching, research, and application development in relevant areas.

Topics to be covered in FDP

- Transition to 3nm and 2nm Nodes using GAA transistors
- VLSI Implementation for Neural Networks
- Perspective of Research in Microelectronics
- VLSI & Embedded Systems for AI/ML and Real-Time Image Processing Applications
- Industrial Exposure about Embedded Systems

Outcomes of the FDP

1. Participants will be able to identify and describe key developments and emerging concepts in VLSI & Embedded Systems.
2. Participants will be able to incorporate contemporary concepts and practices into their teaching, research, or professional activities.

Resource Persons

1. Mr. K. Krishna Mahesh

Technologist, SANDISK India Device Design Centre Pvt. Ltd., Bangalore

2. Smt. T. Venkata Mani

Scientist-G & Technology Director at RCI, DRDO, Hyderabad

3. Dr. T. Sathya Savithri

Prof., Dept. of ECE, UCEST, JNTU Hyderabad

4. Dr. Kishore Sonti

Pro Vice-Chancellor, Jeppiaar University, Chennai.

5. Dr. S. Aruna Mastani

Prof. & Head, Dept. of ECE, JNTUA College of Engineering (A), Ananthapuramu.

6. Dr. M. Durga Prakash

Assoc. Prof., Dept. of ECE, SRM University, AP.

7. Dr. A. Ravi

Prof. & Head, Dept. of ECE, Maulana Azad National Urdu University (Central University), Hyderabad.

For any queries contact:

– Dr. A. Gangadhar, Mobile No.: 9885796156

– Mr. D. Santhosh Gupta, Mobile No.: 9989567744

Chief Patron

Prof. C.S.R.K. Prasad

Hon'ble Vice Chancellor, JNTUK Kakinada.

Patrons

Dr. P. Subba Rao

Rector, JNTUK Kakinada.

Dr. D. Koteswara Rao

OSD to VC, JNTUK Kakinada.

Co-Patron

Dr. R. Srinivasarao

Registrar, JNTUK Kakinada.

Chair Persons

Dr. K. Ravindra

Director, IQAC & CB, JNTUK Kakinada.

Dr. Ch. Srinivasa Rao

Sr. Professor & Principal, UCEN-JNTUK, Narasaraopet.

Convener

Dr. A. M. Prasad

Sr. Professor & HOD of ECE, UCEN-JNTUK, Narasaraopet.

Coordinators

1. Dr. K. Padma Priya

Prof., Dept. of ECE, UCEN-JNTUK, Narasaraopet.

2. Dr. A. Gangadhar

Assoc. Prof., Dept. of ECE, UCEN-JNTUK, Narasaraopet.

Co-Coordiators

1. Mr. D. Santhosh Gupta

Asst. Prof.(c), Dept. of ECE, UCEN-JNTUK, Narasaraopet.

2. Mr. S. V. Sai Prasad

Asst. Prof.(c), Dept. of ECE, UCEN JNTUK, Narasaraopet.

Organizing Committee

1. Mr. V. Harikrishna

Asst. Prof.(c), Dept. of ECE, UCEN-JNTUK, Narasaraopet.

2. Mrs. A. Radha

Asst. Prof.(c), Dept. of ECE, UCEN-JNTUK, Narasaraopet.

3. Mr. Y. Naveen Kumar

Asst. Prof.(c), Dept. of ECE, UCEN-JNTUK, Narasaraopet.

4. Mr. Ch. Krishna Rao Yadav

Asst. Prof.(c), Dept. of ECE, UCEN-JNTUK, Narasaraopet.

Eligibility

This course is open to all the Research Scholars and Faculty Members of Electronics and Communication branch from AICTE approved institutions.

Please note that a minimum of 80% attendance and a score of at least 60% in the assessment test are mandatory for issuing the certificate.

Last Date for registration: 22nd April 2026.

Mode of Program: ONLINE

Timings: 5.00 PM – 8.00 PM

General Information

Other Important Information for participants:

- This FDP is only for faculty & research scholars (Ph.D. & PG) of AICTE approved technical institutions.
- Seats are limited participants are selected by organizers on the basis of first come first serve.

Registration

There is **NO** Registration Fee for JNTUK Affiliated Participants. Visit the Registration link for the details regarding Registration Fee for Non-JNTUK participants.

Registration is mandatory

Link: <https://sites.google.com/jntuk.edu.in/fdp4cet-33/>

Scan to Register

